

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re U.S. Patent Application of)
SHIBAHARA et al.)
Application Number: To be Assigned)
Filed: Concurrently Herewith)
For: PHASE LOCKED LOOP)
ATTORNEY DOCKET NO. HITA.0500)

Honorable Assistant Commissioner
for Patents
Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §§ 1.56 and 1.97, this Information Disclosure Statement is submitted in the above-identified patent application. A listing of documents to be published on the face of any patent granted from this application is submitted herewith on Form PTO-1449. Any other documents or information submitted for consideration by the Examiner are listed in this paper. A copy of each U.S. and foreign patent, or each publication or portion thereof listed or herein identified, is submitted herewith.

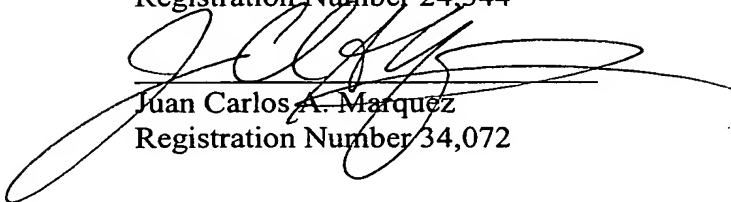
This Information Disclosure Statement is submitted with the initial filing of the application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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Form PTO 1449 U.S. Department of Commerce Patent and Trademark Office Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER HITA.0500	SERIAL NUMBER To be Assigned
	APPLICANT Shibahara et al.	
	FILING DATE Concurrently Herewith	GROUP

U.S. Patent Documents

Examiner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE

Foreign Patent Documents

Examiner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION	
							Yes	No
		2002-271213	3/14/2001	Japan			Abstract	X
		2002-314356	4/11/2001	Japan			Abstract	X
		2003-017985	6/29/2001	Japan			Abstract	X
		2002-353740	5/28/2001	Japan			Abstract	X

Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)

		Seong Hwan Cho et al., "A 6.5GHz CMOS FSK Modulator for Wireless Sensor Applications", 2002 Symposium on VLSI Circuits Digest of Technical Papers, pp. 182-185
		Michael H. Perrott, "A 27-mW CMOS Fractional-N Synthesizer Using Digital Compensation for 2.5-Mb/s GFSK Modulation", IEEE Journal of Solid-State Circuits, Vol. 32, No. 12, December 1997, pp. 2048-2060
		Benzad Razavi, "RF Microelectronics", 1998, Prentice Hall, pp. 278-283

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant